

## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re Application of: CHARLEBOIS ET AL. Attorney Docket No.: BUR920040056US1

Serial No.:

10/709,376

Examiner:

**UNKNOWN** 

Filed:

**APRIL 30, 2004** 

Group Art Unit: 2825

Title: METHOD AND SYSTEM FOR

PERFORMING STATIC TIMING ANALYSIS ON DIGITAL ELECTRONIC CIRCUITS

## LETTER TO THE OFFICIAL DRAFTSMAN

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith are three (3) sheets of formal drawings in the above-identified application.

Respectfully submitted,

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**CERTIFICATE OF MAILING** 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service on the below listed date with sufficient postage for first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By Ciche Digowou Signature